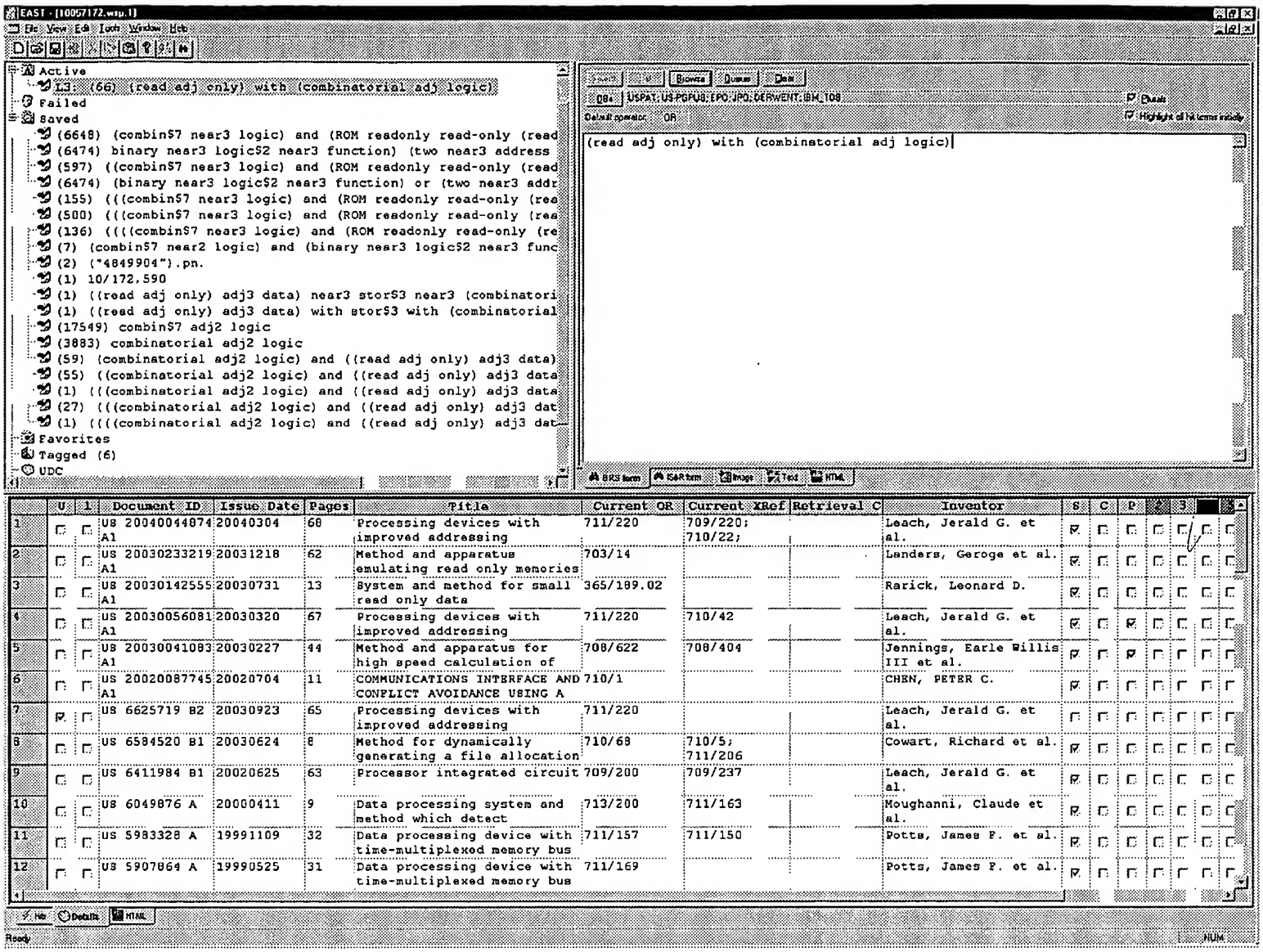

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	2	3
1			US 20040044874 A1	20040304	68	Processing devices with improved addressing	711/220	709/220; 710/22;		Leach, Jerald G. et al.					
2			US 20030233219 A1	20031218	62	Method and apparatus emulating read only memories	703/14			Landers, Geroge et al.					
3			US 20030142555 A1	20030731	13	System and method for small read only data	365/189.02			Rarick, Leonard D.					
4			US 20030056081 A1	20030320	67	Processing devices with improved addressing	711/220	710/42		Leach, Jerald G. et al.					
5			US 20030041083 A1	20030227	44	Method and apparatus for high speed calculation of	708/622	708/404		Jennings, Earle Willis III et al.					
6			US 20020087745 A1	20020704	11	COMMUNICATIONS INTERFACE AND CONFLICT AVOIDANCE USING A	710/1			CHEN, PETER C.					
7			US 6625719 B2	20030923	65	Processing devices with improved addressing	711/220			Leach, Jerald G. et al.					
8			US 6584520 B1	20030624	8	Method for dynamically generating a file allocation	710/68	710/5; 711/206		Cowart, Richard et al.					
9			US 6411984 B1	20020625	63	Processor integrated circuit	709/200	709/237		Leach, Jerald G. et al.					
10			US 6049876 A	20000411	9	Data processing system and method which detect	713/200	711/163		Moughanni, Claude et al.					
11			US 5983328 A	19991109	32	Data processing device with time-multiplexed memory bus	711/157	711/150		Potts, James F. et al.					
12			US 5907864 A	19990525	31	Data processing device with time-multiplexed memory bus	711/169			Potts, James F. et al.					